FY98

No.			
1	3D-IC R&D	*Familiar with 3D-IC design issues and EDA flow *Familiar with reliability issues *Familiar with DFT/BIST and testing technology	*Design methodology and flow integration for 3DIC *3DIC design and physical implementation *Thermal analysis and reduction
2	3D-IC/SoC Design for Testability, Manufacturability & Design for Reliability R&D	*Familiar with SoC design methodology and EDA Flow *Familiar with testable and reliable design/implementation	*Testable design *Reliable design *Fault tolerance design *SoC and 3DIC testing
3	Multi-core Architecture/Software/Applications R&D	*Familiar with Multi-core Architecture *Familiar with RTOS *Familiar with Multi-core software programming	*Multi-core architecture and switching fabric *Multi-core RTOS and software programming
4	Android Platform Architecture/Software/Applications R&D	*Familiar with C/C++ programming *Familiar with Java programming	*C/C++ and Java programming *Multimedia framework programming *Linux kernel porting and device driver development *Open Source GUI Library and browser porting and optimization
5	Electronic System Level (ESL) SoC R&D	*Familiar with SoC design methodology and flow at ESL *Familiar with UNIX, esp. Linux *Familiar with C/C++ programming *Familiar with SystemC programming *Familiar with RTC	*SoC design methodology and EDA flow at ESL *Virtual platform and performance analysis at system level
6	Wireless System Architecture Designer	*Well background knowledge in wireless communication area *Familiar with Networking architecture and System-level simulation technology is a plus	*System architecture design *System Verification
7	Advanced Analog , Mixed-Signal , and RF Circuit R&D/Manager	*Familiar with Digital RF or Low-Voltage/Low-Power RF Circuit *Familiar with Low-Voltage / Low-Power ADC design with digital calibration	*Project Manager *RF/AMS Circuit and System Design
8	System Architect	*Familiar with multimedia algorithm *Familiar with IC/SoC design flow *Device-driver programming capability is preferred	*System-level design for multimedia processing on multi-core platform *Low-power design
9	Multimedia Hardware Design Engineer	*Familiar with IC/SoC design flow *Familiar with multimedia algorithm, implementation, verification and application *Device-driver programming capability is preferred	*AMBA-based multimedia hardware design & verification *Low-power design

No.			
10	Embedded Software Design Engineer	*Familiar with MPU and DSP architecture and programming *Familiar with device driver programming *Familiar with uCOSII or linux embedded os	*SoC related embedded software projects *Dual core multimedia parallel programming *Device driver programming
11	Multicore Architecture/Software/Appliations Designer/Leader	*Familiar with multi-core architecture *Familiar with RTOS *Familiar with multi-core software programming	*Multi-core architecture and switching fabric *Multi-core RTOS and software programming
12	Mixed Signal IC Designer	*Familiar with IC Design Flow and EDA Tools is a must *Familiar with DSP or signal processing is preferred	*Mixed Signal CMOS IC Design, ADC is emphasized *Established Matlab or Verilog-A behavioral model in circuits design
13	RFIC circuit designer	*Familiar with RFIC architecture plan is preferred	*PLL/ADPLL design is emphasized
14	Wireless Communication Protocol Engineer	*Familiar with C language, network communication experience *Working experience on wireless communication protocol development is a plus	* WiMAX protocol software development: MAC protocol, MAC related application, hardware/software co-design *Embedded system development and integration testing * Advanced WiMAX standard contribution and simulation
15	Wireless Communication System Engineer	* Work experience on embedded system development * With knowledge of wireless communication is a plus	* Embedded system development and integration testing *Build up ESL based simulation platform
16	Mobile broadband Communication System and Algorithm Enigneer	*With Communication System knowledge and familiar with C/C++ or Matlab Language	*Link-level simulation *Baseband algorithm design *Base Station DSP Coding and RTL integration
17	Mobile broadband Communication IC Designer	*Familiar with IC Design Flow and related EDA Tools. *Familiar with Wireless communication theory. *With IC design experience is a plus.	*MIMO-OFDM communication hardware architecture design . *WiMAX systemHDL design and verification. *FPGA Prototype and Baseband ASIC design and verification.