



【2011 Summer School Background】

Since the circuit design has been widely recognized as the bedstone of emerging systems and new technology, it is an opportunity for IEEE Circuits and Systems (CASS) to introduce the basics and trends to those students who are interested in devoting their career in this area. This summer school is then encouraged and sponsored by IEEE CASS to carry out this mission. The first IEEE CASS Summer School aims to provide both an objective and clear overview and an in-depth analysis of the state-of-the-art research on a variety of circuit, architecture, and system designs. The lectures will be delivered by distinguished experts in these fields. The summer school also plays the role as a platform for students, early-stage researchers and young scholars to get acquaintance with the experts who have been renowned for what they have achieved.

Dates: Aug. 29 – Sep. 2, 2011

Location : F6019, Dept. of EE, National Sun Yat-Sen University, Kaohsiung, Taiwan

Registration Fee : 100 USD or 3000 NTD (It will be fully refunded if the registered attendee sits in all 8 lectures.)

Registration website : http://vlsi.ee.nsysu.edu.tw/sschool_2011/

Deadline : Aug. 20, 2011

◆ Organizing Co-Chairs

- ◆ **Chua-Ching Wang**
National Sun Yat-Sen University
- ◆ **Rui Martins**
University of Macau
- ◆ **Shyh-Jye Jou**
National Chiao Tung University
- ◆ **Chung-Ho Chen**
National Cheng Kung University

◆ Organizing Committees

- ◆ **Tong-Yu Hsieh**
National Sun Yat-Sen University
- ◆ **Pui-In Mak**
University of Macau
- ◆ **Soon-Jyh Chang**
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Summer School Schedule

	08/29 (Mon)	08/30 (Tue)	08/31 (Wed)	09/01 (Thu)	09/02 (Fri)
09 – 12	Pui-In Mak / Macao University	Katherine Shu-Ming Li / National Sun Yat-Sen University	Tours	Zhiyi Yu / Fudan University	Gwo Giun (Chris) Lee / National Cheng Kung University
Topic	High-/Mixed-Voltage Analog and RF Circuit Techniques for Nanoscale CMOS	Algorithm/Architecture Co-Design for Multiple Scan Trees and Physical Designs		High Performance and Energy-Efficient Many-core Processors for Domain Specific Applications	Algorithm/Architecture Co-Design for Signal Processing Systems
12 – 14	Lunch	Lunch		Lunch	Lunch
14 – 17	Ming-Dou Ker / National Chiao Tung University	Robert Rieger/ National Sun Yat-Sen University		Ke-Horng Chen / National Chiao Tung University	Donald Lie / Texas Tech University
Topic	On-Chip ESD Protection Design in CMOS Integrated Circuits	Integrated Circuit Design for Low-power Biomedical Application		Power Management IC Design	Design of High-Efficiency Si-Based Power Amplifier and Transmitters ICs for Mobile Broadband Wireless Communications

Note : Lunch will be served.

For detailed information, please visit the conference website:

http://vlsi.ee.nsysu.edu.tw/sschool_2011/

Co-Sponsors

